

FIG. 1

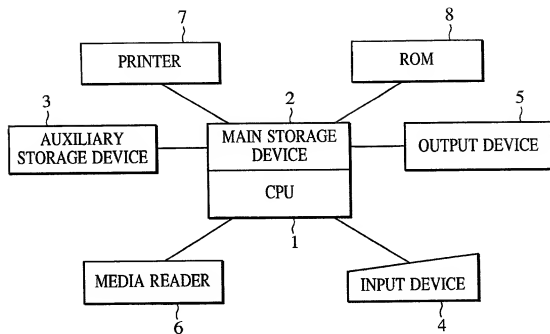


FIG. 2

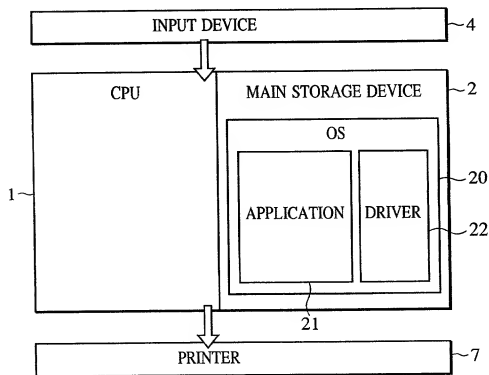


FIG. 3

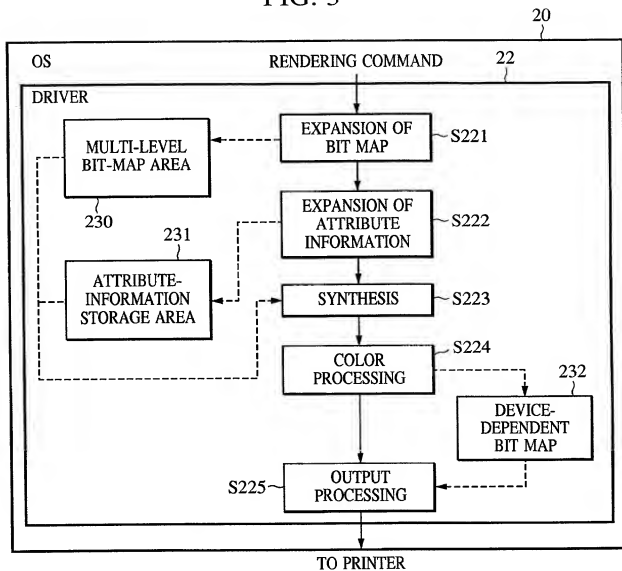
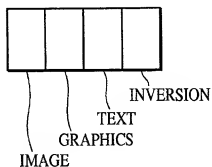
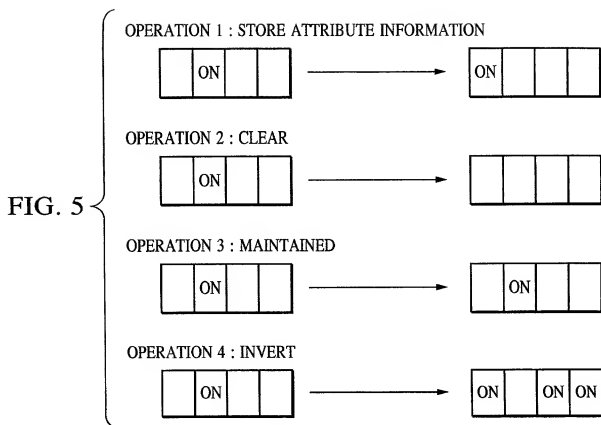


FIG. 4



**FIG. 6**

PATTERN	SOURCE	DEST	RESULT	SOURCE COPY	PATTERN COPY
0	0	0	r000(0 or 1)	0	0
0	0	1	r001(0 or 1)	0	0
0	1	0	r010(0 or 1)	1	0
0	1	1	r011(0 or 1)	1	0
1	0	0	r100(0 or 1)	0	1
1	0	1	r101(0 or 1)	0	1
1	1	0	r110(0 or 1)	1	1
1	1	1	r111(0 or 1)	1	1

FIG. 7A

SOURCE	DEST	RESULT
0	0	r000
0	1	r001
1	0	r010
1	1	r011

FIG. 7B

SOURCE	RESULT		BIT OPERATION ON TARGET ATTRIBUTE
0	r000	}	→ Target_SrcON
0	r001		
1	r010	}	→ Target_SrcOFF
1	r011		

FIG. 7C

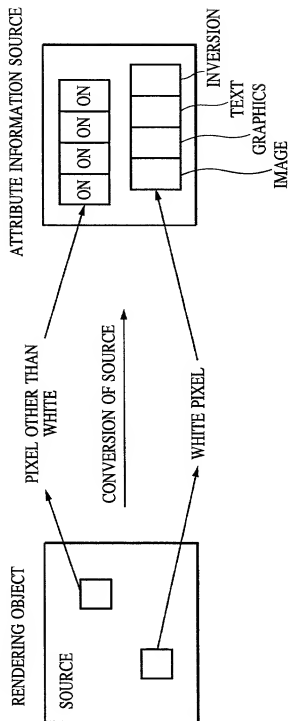


FIG. 7D

SOURCE	RESULT	OPERATION OF TARGET BIT	OPERATION OF NON-TARGET BIT
0	r000	Target_SrcON	NonTarget_SrcON
0	r001		
1	r010	Target_SrcOFF	NonTarget_SrcOFF
1	r011		

FIG. 8A

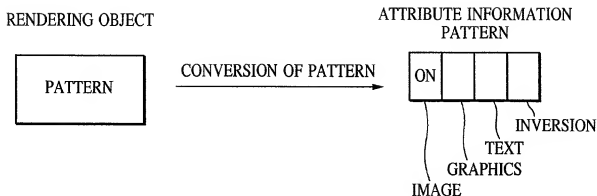


FIG. 8B

ATTRIBUTE INFORMATION PATTERN	ATTRIBUTE INFORMATION SOURCE	ATTRIBUTE- INFORMATION STORAGE AREA	RESULT	
OFF	OFF	OFF	OFF or ON	NonTarget_SrcOFF
OFF	OFF	ON	OFF or ON	
OFF	ON	OFF	OFF or ON	NonTarget_SrcON
OFF	ON	ON	OFF or ON	
ON	OFF	OFF	OFF or ON	Target_SrcOFF
ON	OFF	ON	OFF or ON	
ON	ON	OFF	OFF or ON	Target_SrcON
ON	ON	ON	OFF or ON	

FIG. 8C

ATTRIBUTE-INFORMATION
STORAGE AREA

RESULT

OFF

ON

ON

ON

OPERATION 1 : STORE ATTRIBUTE
INFORMATION

OFF

OFF

ON

OFF

OPERATION 2 : CLEAR

OFF

OFF

ON

ON

OPERATION 3 : MAINTAINED

OFF

ON

ON

OFF

OPERATION 4 : INVERSION

FIG. 9

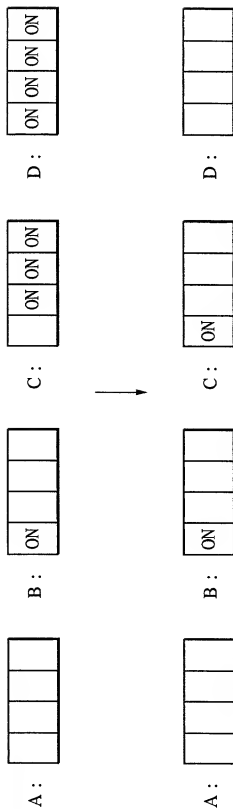


FIG. 10

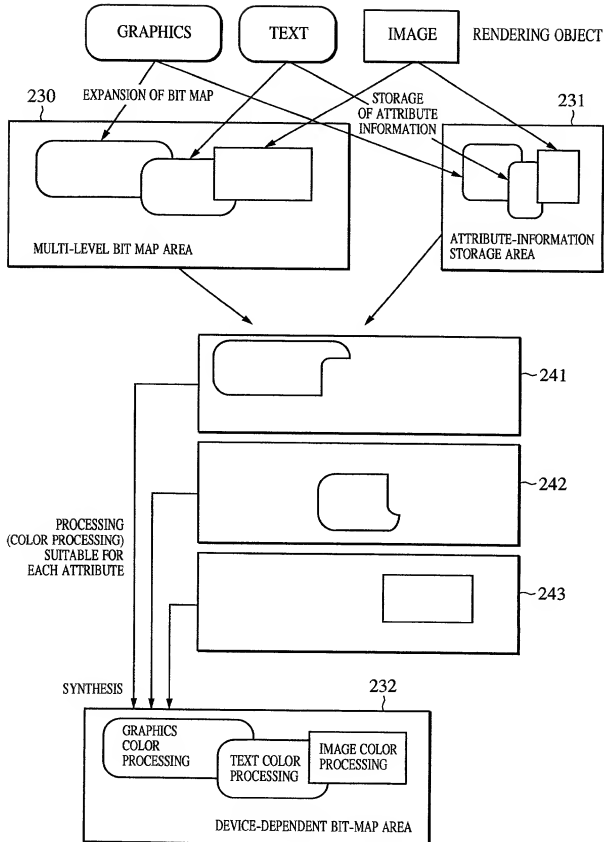


FIG. 11A

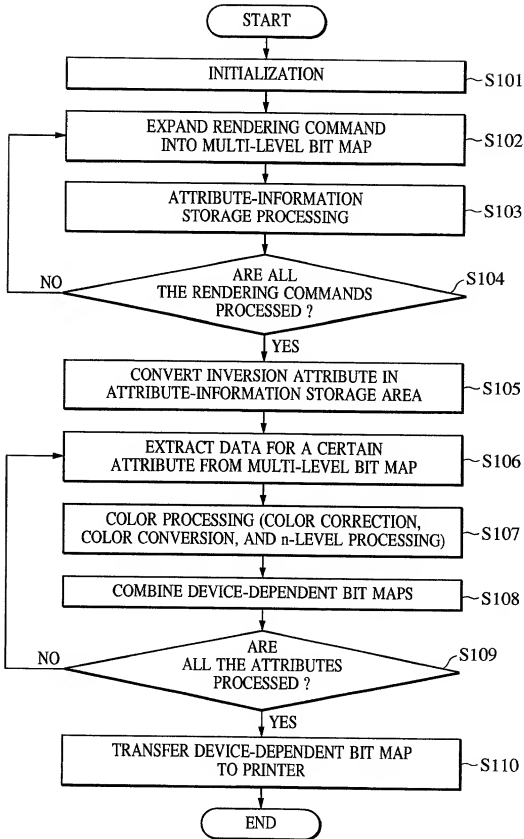


FIG. 11B

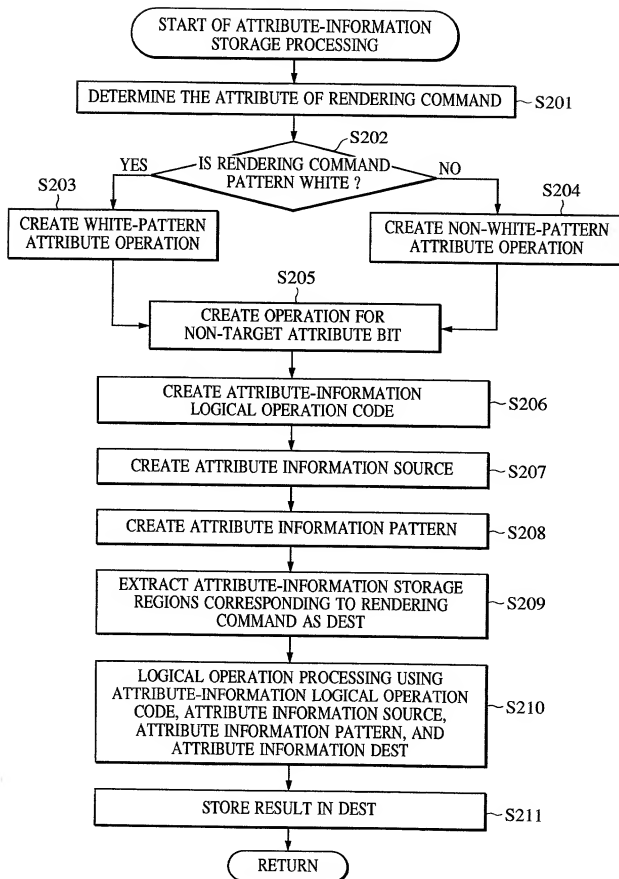


FIG. 12

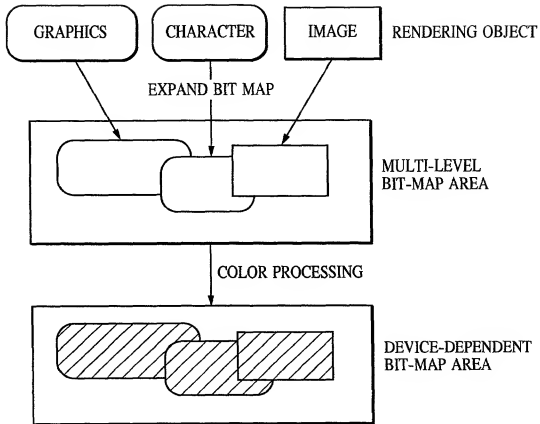


FIG. 13

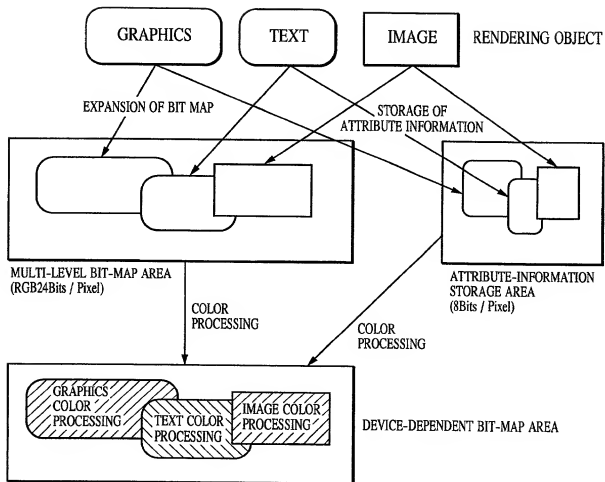


FIG. 14

